

AMENDMENTS TO THE CLAIMS (THIS LISTING REPLACES ALL PRIOR LISTINGS):

1. (Currently Amended) A radar ~~Radar~~ transceiver, ~~containing~~ comprising:
[[-]] ~~at least one~~ an oscillator, ~~which comprises at least one~~ comprising an active circuit component ~~element~~, ~~at least one~~ a resonant circuit, and ~~at least one~~ a circuit component ~~that is applicable~~ for frequency ~~detuning~~ tuning,
[[-]] ~~at least one~~ a mixer ~~comprising at least one~~ comprising a diode and ~~at least one~~ a passive circuit component, and ~~element~~,
[[-]] ~~a substrate (SU) with~~ comprising multiple layers, the multiple layers comprising at least two dielectric layers ~~located directly~~ stacked on top of each other, the substrate having a ~~with~~ metallized top surface, a metallized bottom surface, and metallized internal surfaces ~~being located on top, below and between the dielectric layers,~~
[[-]] ~~wherein an~~ one or more individual electronic components (CB) ~~located~~ component on the metallized top side surface of the substrate comprises (SU), which ~~components comprise~~ [[-]] at least one active or nonlinear circuit component of the mixer and [[-]] at least one active or nonlinear circuit component of the oscillator, and
wherein ~~where~~ the ~~at least single~~ passive circuit ~~element~~ component of the mixer or the ~~at least single~~ resonant circuit of the oscillator is integrated in ~~the~~ one or more metallized surfaces of the substrate. ~~(SU).~~
2. (Currently Amended) The radar ~~Radar~~ transceiver ~~according to Claim~~ of claim 1, wherein the oscillator ~~is~~ comprises a voltage-controlled oscillator (VCO).
3. (Currently Amended) The radar ~~Radar~~ transceiver ~~according Claim 1 or 2 of claim 1~~, wherein the ~~oscillator comprises~~ circuit component for frequency tuning comprises a nonlinear circuit component, ~~element for frequency detuning located on the top side of the substrate.~~

4. (Currently Amended) The radar Radar transceiver according to Claim 3 of claim 1, wherein the ~~nonlinear~~ circuit ~~element~~ component for frequency ~~detuning~~ tuning comprises ~~is~~ a varactor diode.
5. (Currently Amended) The radar Radar transceiver according to at least one of the Claims 1 to 4 of claim 1, wherein the mixer ~~contains~~ comprises a hybrid ring that is integrated in the substrate. ~~(SU).~~
6. (Currently Amended) The radar Radar transceiver according to at least one of the Claims 1 to 5 of claim 1, further comprising a frequency divider ~~(FD)~~ for dividing ~~the a~~ frequency of ~~the an~~ output signal of the oscillator.
7. (Currently Amended) The radar Radar transceiver according to at least one of the Claims 1 to 6 of claim 6, wherein the frequency divider comprises ~~comprising~~ a phase-locked loop, ~~which is integrated in the circuit of the frequency divider.~~
8. (Currently Amended) The radar Radar transceiver according to at least one of the Claims 1 to 7 of claim 1, wherein the metallized bottom surface of the substrate comprises having a terminal on the bottom side of the substrate for connecting connection for connecting to an external antenna.
9. (Currently Amended) The radar Radar transceiver according to at least one of the Claims 1 to 8 of claim 1, further comprising ~~wherein at least~~ a part of at least one antenna ~~(TX ANT, RX ANT)~~ that is located on the top side metallized surface of the substrate or the bottom ~~side~~ metallized surface of the substrate.
10. (Currently Amended) The radar Radar transceiver according to at least one of the Claims 1 to 9 of claim 1, further comprising ~~at least one a~~ cover film ~~(SF), which covers~~

~~for covering the one or more individual electronic component at least partly components completely and serves the purpose of protecting one or more individual electronic components from dust, humidity and mechanical effects.~~

11. (Currently Amended) The radar Radar transceiver according to Claim 10 of claim 10, further comprising wherein the cover film is covered by a metal layer that at least partly covers the cover film.

12. (Currently Amended) The radar Radar transceiver according to at least one of the Claims 1 to 11 of claim 10, which further comprising is encased by a casting resin that at least partly encases the cover film.

13. (Currently Amended) The radar Radar transceiver according to at least one of the Claims 1 to 12 of claim 1, which contains wherein at least one circuit element (IE) selected from among an inductance, a capacitance, a line or line termination that is integrated in the substrate. (SU).

14. (Currently Amended) The radar Radar transceiver according to at least one of the Claims 1 to 13 of claim 1, wherein the one or more individual electronic component comprises components (CB) on the top side of the substrate (SU) are selected from among a microwave chip, a millimeter wave chip or an IC integrated circuit element.

15. (Currently Amended) The radar Radar transceiver according to Claim claim 14, wherein the at least single IC integrated circuit element comprises a represents an MMIC [[-]] monolithic microwave integrated circuit [[-]] element.

16. (Currently Amended) The radar Radar transceiver according to at least one of the Claims 1 to 15 of claim 1, wherein the one or more individual electronic component is

~~components are~~ mechanically and electrically connected to the substrate (SU) via flip chip technology or ~~SMD~~ surface mounted device technology.

17. (Currently Amended) The radar ~~Radar~~ transceiver ~~according to at least one of the Claims 1 to 16 of claim 1,~~ further comprising one or more ~~individual~~ electronic components (~~CB~~), selected from among the following components: a discrete passive circuit element including a coil, a capacitor and a resistor, or which presents a compact circuit block, which contains at least one individual electronic component selected from among a coil, a capacitor or a resistor, including any combination of ~~said~~ individual components.

18. (Currently Amended) The radar ~~Radar~~ transceiver ~~according to at least one of the Claims 1 to 17 of claim 1,~~ wherein the substrate (SU) ~~contains~~ comprises at least two layers of ~~LTCC or HTCC ceramic~~ LTCC or HTCC ceramic ~~[[-]]~~ low temperature cofired ceramic, or high temperature cofired ceramic.

19. (Currently Amended) The radar ~~Radar~~ transceiver ~~according to at least one of the Claims 14 to 18 of claim 1, which contains at least one~~ further comprising:
 a mixer diode or ~~at least one~~ a chip element that performs, ~~which accomplishes~~ a mixer function~~[[,]]~~; and
 a IC integrated circuit element, ~~which comprises that comprises~~ at least a part of the oscillator and ~~a the~~ frequency divider, (~~FD~~).

20. (Currently Amended) The radar ~~Radar~~ transceiver ~~according to at least one of the Claims 14 to 19 of claim 1,~~ wherein at least a part of the oscillator, ~~the~~ a frequency divider, (~~FD~~) and the mixer is ~~realized~~ provided in one, two or three IC integrated circuit elements.

21. (Currently Amended) ~~The radar~~ Radar transceiver ~~according to at least one of the Claims 1 to 20 of claim 1~~, wherein frequency modulation occurs via ~~takes place by means of~~ frequency keying of ~~an~~ the oscillator, an amplifier associated with the radar transceiver, or a very high frequency switch associated with the radar transceiver.

22. (Currently Amended) ~~The radar~~ Radar transceiver ~~according to at least one of the Claims 1 to 21 of claim 1~~, wherein amplitude modulation occurs via ~~takes place by means of~~ amplitude keying of the oscillator, an amplifier associated with the radar transceiver, or a very high frequency switch associated with the radar transceiver.

23. (Currently Amended) ~~The radar~~ Radar transceiver ~~according to at least one of the Claims 13 to 22 of claim 1~~, wherein ~~the at least single~~ further comprising an integrated circuit IC element comprises at least one comprising an amplifier that is in the a transmission or reception path of the radar transceiver.

24. (Currently Amended) ~~The radar~~ Radar transceiver ~~according to at least one of the Claims 1 to 23 of claim 1~~, ~~which wherein the radar transceiver comprises is configured as~~ a low temperature cofired ceramic an LTCC module or as partial modules that are electrically connected with each other, where said partial modules are installed by machine using SMD surface mounted device technology.

25. (Currently Amended) ~~The radar~~ Radar transceiver ~~according to Claim 1 of claim 1~~, wherein the substrate comprises (SU) ~~is~~ as a monolithic ceramic object.

26. (Currently Amended) ~~The radar~~ Radar transceiver ~~according to Claim 1 of claim 1~~, wherein the ~~at least single~~ passive circuit element component of the mixer, ~~and/or the at least single~~ resonant circuit of the oscillator, or both, are ~~is~~ at least partially integrated in at least one of the internal metallized surfaces surface of the substrate, (SU).